# **Refine Search**

#### Search Results -

| Terms      | Documents |
|------------|-----------|
| L5 and L26 | 1         |

Database:

US Pre-Grant Publication Full-Text Database
US Patents Full-Text Database
US OCR Full-Text Database
EPO Abstracts Database
JPO Abstracts Database
Derwent World Patents Index
IBM Technical Disclosure Bulletins

Search:

L27

|             |  | 垃 | Refine Search |
|-------------|--|---|---------------|
| Pecall Toxt | - Septimental Control of the Control |   | Intorrupt     |

#### **Search History**

## DATE: Tuesday, October 12, 2004 Printable Copy Create Case

| Set<br>Name<br>side by<br>side | Query   | Hit<br>Count | Set<br>Name<br>result set |
|--------------------------------|---|--------------|---------------------------|
| DB=P                           | GPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR=YES; OP=ADJ               |              |                           |
| <u>L27</u>                     | 15 and 126  | 1            | <u>L27</u>                |
| <u>L26</u>                     | l21 and l11   | 16           | <u>L26</u>                |
| <u>L25</u>                     | l21 and l22   | 1            | <u>L25</u>                |
| <u>L24</u>                     | (second instruction\$1) same (pointer\$1 near3 (exception handler\$1))  | 2            | <u>L24</u>                |
| <u>L23</u>                     | (second instruction\$1) near3 (pointer\$1 near3 (exception handler\$1)) | 2            | <u>L23</u>                |
| <u>L22</u>                     | (program counter\$1) near3 (exception handler\$1)                       | 30           | <u>L22</u>                |
| <u>L21</u>                     | (program counter\$1) near3 (determin\$5 near2 location\$1)              | 32           | <u>L21</u>                |
| <u>L20</u>                     | (rogram counter\$1) near3 (determin\$5 near2 location\$1)               | 0            | <u>L20</u>                |
| <u>L19</u>                     | 11 and 12   | 21           | <u>L19</u>                |
| <u>L18</u>                     | 11 and 15   | 23           | <u>L18</u>                |
| <u>L17</u>                     | 12 and 15   | 2            | <u>L17</u>                |
| <u>L16</u>                     | 12 and 114  | 9            | <u>L16</u>                |
| <u>L15</u>                     | L10 and 11  | 14           | <u>L15</u>                |

| <u>L14</u> | L11 and 11   | 116   | <u>L14</u> |
|------------|--|-------|------------|
| <u>L13</u> | L12 and 11   | 3     | <u>L13</u> |
| <u>L12</u> | 709/\$.ccls.   | 31547 | <u>L12</u> |
| <u>L11</u> | 712/\$.ccls.   | 10962 | <u>L11</u> |
| <u>L10</u> | 714/\$.ccls.   | 46633 | <u>L10</u> |
| <u>L9</u>  | (ia64 instruction\$1) same (unused bit space\$1)                                     | 0     | <u>L9</u>  |
| <u>L8</u>  | (exception-type\$1) near3 handler\$1   | 1     | <u>L8</u>  |
| <u>L7</u>  | (second instruction\$1) same (no op instruction\$1)                                  | 9     | <u>L7</u>  |
| <u>L6</u>  | instruction\$1 same (non-executable bit\$1)  | 1     | <u>L6</u>  |
| <u>L5</u>  | ((first instruction\$1) same (second instruction\$1)) same (offset\$1)               | 153   | <u>L5</u>  |
| <u>L4</u>  | (instruction\$1 near2 bundle\$1) same (exception\$1 handler\$1)                      | 3     | <u>L4</u>  |
| <u>L3</u>  | ((first instruction\$1) same (second instruction\$1)) same (exception\$1 handler\$1) | 7     | <u>L3</u>  |
| <u>L2</u>  | instruction\$1 same (pointer\$1 near3 (exception\$1 handler\$1))                     | 25    | <u>L2</u>  |
| L1         | instruction\$1 same (pointer\$1 near3 exception\$1)                                  | 173   | L1         |

### END OF SEARCH HISTORY